	Application No.	Applicant(s)
Notice of Allowability	09/778,104	HIRANO ET AL.
	Examiner	Art Unit
	Victor A Mandala Jr.	2826
The MAILING DATE of this communication appears on the cover sheet with the correspondence address- All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308. 1. A This communication is responsive to Afterlinal 8/15/03. 2. The allowed claim(s) is/are 1-13. 3. The drawings filed on are accepted by the Examiner.		
2. The allowed claim(s) is/are 1-13.		NATION W
3. The drawings filed on are accepted by the Examine	r. GUPE	CHNOLOGY CLINTER
4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). TECHNOLOGY a) All b) Some of the:		
 Certified copies of the priority documents have been received. 		
2. Certified copies of the priority documents have been received in Application No		
 Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)). 		
* Certified copies not received:		
5. Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).		
(a) The translation of the foreign language provisional application has been received.		
6, 🗌 Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.		
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE		
7. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.		
⊠ CORRECTED DRAWINGS must be submitted. (a) ☐ including changes required by the Notice of Draftspers 1) ☐ hereto or 2) ☐ to Paper No	,	, e
(b) \(\infty\) including changes required by the proposed drawing correction filed \(\frac{20 \) February 2003,}{20.5}\) which has been approved by the Examiner.		
(c) 🔲 including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No		
Identifying indicia such as the application number (see 37 CFR 1. each sheet.	84(c)) should be written on the drawi	ngs in the front (not the back) of
9. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT FOR TI		
Attachm nt(s)		
1⊠ Notice of References Cited (PTO-892) 3☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 5☐ Information Disclosure Statements (PTO-1449), Paper No	4 Interview Summ	
7 Examiner's Comment Regarding Requirement for Deposit of Biological Material	8⊠ Examiner's State 9∐ Other	ement of Reasons for Allowance

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DETAILED ACTION

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or
additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR
 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the
payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Ron Rudder on 9/23/03.

The application has been amended as follows: Claims 12-13 are cancelled.

Allowable Subject Matter

- Claims 1-11 are allowed. Claims 3-4 and 7-13, which were withdrew due to an election/ restriction requirement on 9/22/02, are rejoined because of the generic claim being made allowable.
- 3. The following is a statement of reasons for the indication of allowable subject matter:
 The prior art teaches a transistor on an SOI substrate, where the there is a source, a drain, a gate, a gate dielectric, an interlayer dielectric layer formed on the transistor a drain and source wiring formed on the interlayer dielectric layer, a first conductor formed in the interlayer dielectric layer connecting the drain to the drain wiring, and a second conductor formed in the interlayer dielectric layer connecting the source to the source wiring. The prior art does not teach the prior art in combination with the drain and source regions independently having a first area on either

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side of the channel, where there is a second region that protrudes from the first region away from

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the channel wherein the first and second conductors electrically connect to the second protruding

areas which in return reduces the capacitance between the source and drain conductors and the

gate electrode.

Conclusion

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Victor A Mandala Jr. whose telephone number is (703) 308-6560.

The examiner can normally be reached on Monday through Thursday from 8am till 6pm..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Nathan Flynn can be reached on (703) 308-6601. The fax phone number for the

organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding

should be directed to the receptionist whose telephone number is (703) 308-0956.

VAMJ

9/21/03